



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: **Jia LEE et al.**

Group Art Unit: **2819**

Serial No.: **10/812,419**

Examiner: **James Hyonchol Cho**

Filed: **March 30, 2004**

Confirmation No.: **1267**

For: **REVERSIBLE LOGIC ELEMENTS OPERATING IN ASYNCHRONOUS MODE**

Attorney Docket Number: **042131**

Customer Number: **38834**

**SUBMISSION OF REPLACEMENT DRAWINGS**

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

Date: October 4, 2005

Sir:

Applicants submit herewith 11 sheets of replacement drawings depicting Figures 1-12 for the above-identified application.

Applicants respectfully request that the replacement drawings submitted herewith be approved by the Examiner.

If any fees are required in connection with this submission, please charge our Deposit Account No. 50-2866.

Respectfully submitted,

**WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP**

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